

IN THE SPECIFICATION

Please Replace Paragraph 0023.1 with following paragraph:

[0023.1] Referring now to **FIGURE 5**, there is illustrated a block diagram describing an exemplary circuit 500 for decoding video data in accordance with an embodiment of the present invention. A video decoder 505 includes a function commonly referred to as "motion compensation." This function is used to allow the video decoder 505 to process numerous video compression standards, including but not necessarily limited to: MPEG-1, MPEG-2, MPEG-4, H.263, H.264, and H.261. More specifically, motion compensation includes a process of copying a two-dimensional block 510 of image data from a previously decoded reference frame 515 to the frame 520 currently being decoded. The location of the reference block 517 relative to the current position in the current frame is specified by "motion vectors" included within the input code stream 525. Motion compensation allows for a compact specification of the data whenever the video stream is well modeled by translational motion. The reference frame 515 that is used for motion compensation is stored in a relatively large memory, such as DRAM 530. The DRAM 530 can be configured to store the reference frame 515 in the manner described in FIGURES 3 and 4. In one embodiment, segments of data with better alignment with respect to the burst boundaries can be determined by combinatorial logic in the video decoder 505.